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obstitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number 10/683,929

Filing Date October 9, 2003

First Named Inventor John W. Rapp

Art Unit 2818

Examiner Name

(Use as many sheets as necessary)

Sheet 1 of 2 Attorney Docket Number 1934-13-3

	U.S. PATENT DOCUMENTS							
Examiner	Cite	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant			
initials No.	No.	Number - Kind Code <sup>2</sup> (if known)	MM-DD-YYYY	Cited Document	Passages or Relevant Figures Appear			
/DJH/		US-6,704,816 B1	03/09/2004	Burke				
/DJH/		US- 2004/181621 A1	09/16/2004	Mathur, et al.				
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	FOREIGN PATENT DOCUMENTS						
Examiner	Cite	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines, Where Relevant		
Initials*	No.'	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (# known)	Date Applicant of Cited MM-DD-YYYY Document	Applicant of Cited Document	Passages or Relevant Figures Appear	T <sup>0</sup>	
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Examiner Signature	/David Huisman/	Date Considered	05/07/2007
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-	Substitute	for form 144	98/PTO		Complete if Known				
			<b>~~~</b>	001.001105	Application Number	10/683,929			
_			_	SCLOSURE	Filing Date	October 9, 2003			
	STATEMENT BY APPLICANT				First Named Inventor	John W. Rapp			
					Art Unit	2818			
	(Use as many sheets as necessary)				Examiner Name				
	Sheet	2	of	2	Attorney Docket Number	1934-13-3			

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
/DJH/		International Search Report for PCT/US2005/035818 dated August 4, 2008		
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Examiner Signature	/David Huisman/	Date Considered	05/07/2007

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'Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449B/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/683,929 Filing Date October 9, 2003 First Named Inventor John W. Rapp Art Unit 2818 Examiner Name

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		NON PATENT LITERATURE DOCUMENTS	,
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
/DJH/		BAKSHI S; GAJSKI D D; "Partitioning and Pipelining for Performance-Constrained Hardware/Software Systems", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 7, NR. 4, PG. 419-432, (1999-12-00), XP000869229	
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and not considered. Include copy of this form with next communication to applicant.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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	Complete if Known	
Application Number	10/683,929	
Filing Date	October 9, 2003	
First Named Inventor	John W. Rapp	
Art Unit	2818	
Examiner Name		
Attorney Docket Number	1934-13-3	

*	U.S. PATENT DOCUMENTS							
Examiner	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant			
		Number - Kind Code <sup>2</sup> (if known)	MM-DD-YYYY		Passages or Relevant Figures Appear			
· /DJH/		US-5,583,964	12/10/96	Wang				
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		FOREIGN PA	TENT DOCU	MENTS		
Examiner	Cite	Foreign Patent Document	Publication	Name of Patentee or	Pages, Columns, Lines,	
Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>6</sup> ( <i>il known</i> )	Date MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	۳⁵
/DJH/		EP 1 061 438 A1	12/20/00	Hewlett- Packard Company		
/DJH/		EP 0 945 788 A2	09/29/99	Texas Instruments Incorporated		
/DJH/		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated		
/DJH/		EP 0 694 847 A2	01/31/96	International Business Machines Corporation		
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Substitute for form 14498/PTO Complete if Known **Application Number** 10/683,929 INFORMATION DISCLOSURE Filing Date October 9, 2003 STATEMENT BY APPLICANT First Named Inventor John W. Rapp Art Unit 2818 (Use as many sheets as necessary) Examiner Name Sheet of | 2 Attorney Docket Number 1934-13-3

NON PATENT LITERATURE DOCUMENTS					
Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of					
	LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103				
	VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33.  Proceedings of the 33 <sup>rd</sup> Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc, US, 10 December 2000, pp. 171-177				
	International Search Report for PCT/US 03/34557, December 21, 2004				
	International Search Report for PCT/US 03/34559, January 7, 2005				
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		No.¹  number(s), publisher, city and/or country where published.  LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 55, 1996, pp. 98, 101-103  VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 <sup>rd</sup> Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamiltos, CA: IEEE Comp. Soc, US, 10 December 2000, pp. 171-177  International Search Report for PCT/US 03/34557, December 21, 2004			

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